



RajaRajeswari College of Engineering

#14, Ramohalli cross, Mysore Road, Kumbalgodu, Bangalore-74

Department of Electronics & Communication Engineering

Report on 5 Days STP

Objective:

The fundamentals of embedded system hardware and firmware design will be explored. The architecture and instruction set of the microcontroller will be discussed, and a wire wrapped microcontroller board will be built and debugged by each student

1. **Type of Event:** Five Days Student Training Program (27/03/2017 to 31/03/2017)

2. **Title of the Event:** "Embedded System Design"

- **Resource Persons:** 1. G. Prasanth Reddy, Team Leader, Intel India, Bangalore
2. L. Swarnajothi, Prof & HOD of ECE
3. Kumaraswamy K.V., Technical Manager, Tech Labs, Bangalore
4. Soven K Dana., Prof of ECE
- **Duration:** 5 Days
- **Venue:** APJ Abdul Kalam Hall, 2nd Floor, Rajarajeswari Engineering College.
- **Target Participants :** 2nd, 3rd Year and 4 Year students
- **Co-ordinators:** Prof. L. Rangaiah ECE Dept., Prof. V. Sreepathi

Day & Date	Time	Topic
Day1 27/03/17	Morning Session : 9.00 am-12.00pm	Introduction to High End Microcontroller
	Afternoon Session: 1.00pm- 4.00 pm	Fundamentals of Embedded system hardware
Day2 28/03/17	Morning Session: 9.00 am- 12.00 pm	Managing projects in Embedded systems
	Afternoon Session: 1.00pm- 4.00 pm	Recent technologies in Embedded Systems
Day3 29/03/17	Morning Session : 9.00 am-12.00pm	High End Microcontroller such as Arm Cortex
	Afternoon Session: 1.00pm- 4.00 pm	High End Microcontroller such as MSP430
Day4 30/03/17	Morning Session : 9.00 am-12.00pm	Python programming for Embedded design
	Afternoon Session: 1.00pm- 4.00 pm	Python programming for Embedded design
Day5 31/03/17	Morning Session : 9.00 am-12.00pm	Python programming applications
	Afternoon Session: 1.00pm- 4.00 pm	Program execution in Python