

# **Alumni Association - RajaRajeswari College of Engineering**

## **Department of Electronics and Communication Engineering**

### **2023-24 Alumni Talk – A Report**

**Date: 25/011/2023**

<b>Event Name</b>	<b>: A Talk on " Motherboard peripherals connections and functions "</b>
<b>Venue</b>	<b>: Satyendranath Seminar Hall</b>
<b>Date &amp; Time</b>	<b>: 25/11/2023 &amp; 10.30am to 11.30am</b>
<b>Alumnus</b>	<b>: PRARTHANA D RAJ</b>
<b>Batch</b>	<b>: 2018 passed</b>
<b>Organization</b>	<b>: UST GLOBAL</b>
<b>Designation</b>	<b>: Debug Engineer</b>

### **Summary of the event:**

- ✚ The event started with welcoming the alumnus for the event by HoD-Dr.L Rangaiah, Dr.S.M Vijaya, Prof.Haritha, Prof.Shripathi., II year and IV year ECE Engineering students participated.
- ✚ Around 65 students from both the classes participated in the event. She went through all the challenges he came across in her engineering life that let her down and later told about the moral support she got from his mentors that made her stand in this position today.
- ✚ She let us know about the Soc validation and debugging for troubleshooting hardware and software issues.
- ✚ She even told us about the pre silicon and post silicon environments.
- ✚ She even guide us about the importance of Communication skills and presentation skills one should have while entering the industries.
- ✚ After the interaction with the students, the resource person was honored with Memento as a token of appreciation to conclude the event.

## Event poster

**RAJARAJESWARI COLLEGE OF ENGINEERING**  
AFFILIATED TO VTU, BELGAVI. APPROVED BY AICTE, NEW DELHI & GOVT. OF KARNATAKA

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**



**"Technical talk on Motherboard peripherals connections and functions"**

**Contents:**

- Soc validation and debugging for troubleshooting hardware and software issues.
- Pre silicon and Postsilicon environments.
- Communication skill and Presentation skill

**Venue:**  
**Satyendranath Bose Hall**  
**25/11/2023**  
**Time: 11 am**

**Prarthana D Raj**  
(RRCE- 2014 Batch)  
Debug Engineer UST  
GLOBAL

**Dr.Rangaiah L**  
Prof. &Head  
Dept. of ECE

**Dr. R Balakrishna**  
Principal

**Co-ordinators:**  
**Dr. S.M.Vijaya.**  
Associate Professor  
**Prof. Sreepathi**  
Asst. Professor

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## Photos of the Event



**Mrs. PRARTHANA D RAJ**  
Interacting with Students about herself



**Mrs. PRARTHANA D RAJ**  
Introduces to the student friends



**Alumnus interaction with the students & honored with Memento as a token of appreciation**